

Gate that produces a logic 1 when all of its inputs are 1. In all other cases the output is 0.

From:  
<http://wiki.midibox.org/> - **MIDIbox**



Permanent link:  
<http://wiki.midibox.org/doku.php?id=and&rev=1160904901>

Last update: **2007/11/17 17:53**