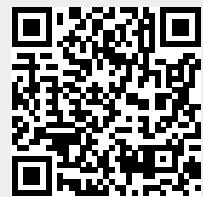


The number of data bits that can be input or accessed simultaneously. Common bus widths of DRAM are 32, 36, 40, 64, 72 and 80 bits. See BUS.

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