

An acronym for Field Effect Transistor. It is a bipolar transistor is a current amplifying device. An input current results in an amplified output current  $H_{fe}=I_c/I_b$ , where  $I_c$  is the output or collector current and  $I_b$  is the input or base current. The FET on the other hand is a transconductance device, that is an input voltage results in an output current  $G=I_d/V_g$ , where  $I_d$  is the output or drain current and  $V_g$  is the input or gate voltage. This assumes a common emitter connection for the bipolar transistor and a common source connection for the FET.

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