SC and RC Lines
between J1 and J2 should "not"
be directly connected together
but should "follow" the long SRIO chain to consider the signal runtimes!

B:
SN74LVC1G17DBVR
act as Buffer for
SRIO-Chain

Only solder RT1
when BLM
is last in
chain

Sheet: /	
File: PCB.kicad_sch	
Title: BLM_SCALAR + BLM16x16+X	
Size: A1	Date: 2022-12-18
Rev: a	