

# AIN\_4 module

This is a simple way of scaling CVs (as might come from a modular) ready for an STM32F4 Core module.

## Schematic



The circuit is a very simple series of two inverting amplifiers per channel. The first is a TL072 powered from +12/-12V, while the second is an MCP6002 powered from 3v3/0V (ground). It's possible to preset the ranges (see below) but it's probably more useful to use the associated [Control Board](#).

## BOM

| Type              | Qty | Value    | Package            | Parts   | Notes   |
|-------------------|-----|----------|--------------------|---|---|
| <b>resistors</b>  |     |          |                    |   |   |
|                   | 20  | 49k9     | 0204/7             | R1, R2, R4, R5, R6, R7, R8, R10, R11, R12, R13, R14, R16, R17, R18, R19, R20, R22, R23, R24 |   |
|                   | 4   | 30k      | 0204/7             | R3, R9, R15, R21  | nominal gain can be adjusted e.g. $(22k/49.9k) * 5V = 2.2V$ |
| <b>capacitors</b> |     |          |                    |   |   |
|                   | 4   | 10p      | 025x050            | C1, C3, C5, C7  |   |
|                   | 9   | 100n     | 025x050            | C10, C12, C13, C14, C15, C16, C17, C18, C20   | remove 1 if powering from Core                              |
|                   | 3   | 10u      | electrolytic 2,5-6 | C9, C11, C19  | remove 1 if powering from Core                              |
|                   | 4   | optional | 025x050            | C2, C4, C6, C8  | can use another e.g. 10pF cap here for more filtering       |

| Type             | Qty | Value    | Package        | Parts  | Notes  |
|------------------|-----|----------|----------------|--|--|
| <b>resistors</b> |     |          |                |  |  |
| <b>inductors</b> |     |          |                |  |  |
|                  | 2   | BEAD     | 5MM            | L1, L2                                       |  |
| <b>ICs</b>       |     |          |                |  |  |
|                  | 2   | MCP6002P | DIL08          | IC2, IC4                                     | sockets are recommended                            |
|                  | 2   | TL072P   | DIL08          | IC1, IC3                                     | sockets are recommended                            |
| <b>Vreg</b>      |     |          |                |  |  |
|                  | 1   | optional | TO-220         | VR1  | remove if powering from Core                       |
| <b>headers</b>   |     |          |                |  |  |
|                  | 7   |          | 1X02_SMALL     | JP1, JP2, JP3, JP4, JP5, JP6, JP7            | check board for headers as SIL strips of 3, 5 or 6 |
|                  | 8   |          | 1X03_SMALL     | JP8, JP9, JP10, JP11, JP12, JP13, JP14, JP15 |  |
|                  | 2   |          | 2*5 (shrouded) | J1, J2                                       |  |

## Assembly

Putting the board together is simple. As usual, start with the flattest components and work your way up.

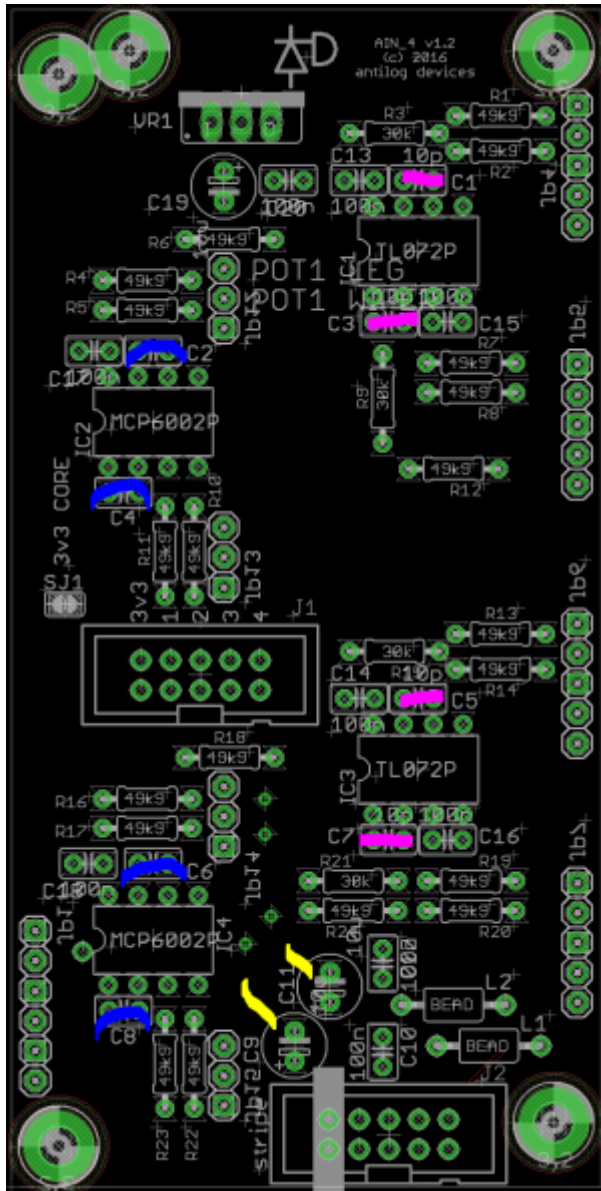
### Resistors

Four 30k resistors as marked, the remainder are 49k9. For best results, match R1/R2, R7/R8 (and so on), and R4/R5/R6, R10/R11/R12 (and so on). The value is less important than the minimum deviation.



## Capacitors

Four 10p as marked in pink, electrolytics in yellow, blue are optional. Remainder are 10n.



## Power

Standard Eurorack header. Red = -12V, Blue = +12V, Green = 0V(ground). For Core power, bridge the yellow solder jumper, otherwise install the Vreg circuit in pink

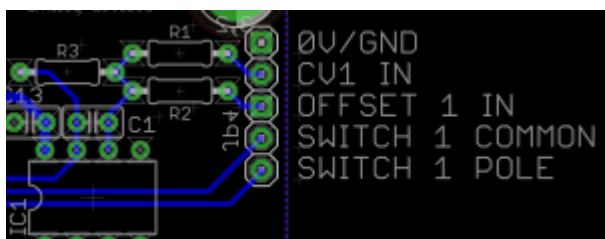


### Standalone use

For simplest results, use the complementary [Control Board](#). Otherwise see below for header functions.

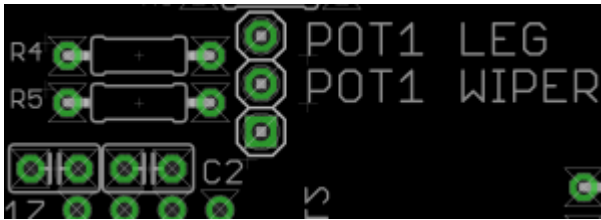
### Five pin header

For scaling bipolar CVs, connect or switch into OFFSET (e.g. +5V)  
For 0-5V operation, leave SWITCH open; for 0-10V operation, jumper or switch in SWITCH



### Three pin header

With a 100k pot facing you, solder the left leg to 0V(ground) the centre to WIPER and the right to LEG. WIPER and LEG may be jumpered if pots aren't desired, but it is quite useful to have an attenuator to trim down variable CVs or even clip the second op amp for crunchy waveforms!



### Interconnection to Core

J1 carries the scaled CV to J5A or J5B of an STM32F4 Core module.

### License

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