An acronym for Field Programmable Gate Array. This is a logic device in the PLD family. A generic description of an FPGA is a programmable device with an internal array of logic blocks, surrounded by a ring of programmable input/output (I/O) blocks, connected together via programmable interconnect (FPIC). There are a wide variety of sub-architectures within this group. The secret to density and performance in these devices lies in the logic contained in their logic blocks and on the performance and efficiency of their routing architecture.

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Last update: **2011/07/21 13:08**