

MIDIBox64E is a midi controlled based on [MBHP/MIOS](#)

- [MIDIbox 64E](#) <sup>uCApps</sup>
- [MIDIbox 64E Tutorial](#) <sup>uCApps</sup>
- [MIDIbox64\(E\) Patchmode](#)

## DIN register allocation

Question from Dan aka Digneural:

I understand that the following is setting shift registers 5-8 to be used for encoders 1-16.

MIOS\_ENC\_PIN\_TABLE

```
;; encoders 1-16
;;      SR  Pin  Mode
ENC_ENTRY 5,  0,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 1
ENC_ENTRY 5,  2,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 2
ENC_ENTRY 5,  4,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 3
ENC_ENTRY 5,  6,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 4
ENC_ENTRY 6,  0,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 5
ENC_ENTRY 6,  2,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 6
ENC_ENTRY 6,  4,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 7
ENC_ENTRY 6,  6,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 8
ENC_ENTRY 7,  0,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 9
ENC_ENTRY 7,  2,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 10
ENC_ENTRY 7,  4,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 11
ENC_ENTRY 7,  6,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 12
ENC_ENTRY 8,  0,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 13
ENC_ENTRY 8,  2,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 14
ENC_ENTRY 8,  4,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 15
ENC_ENTRY 8,  6,  MIOS_ENC_MODE_NON_DETENTED ; V-Pot 16
```

However I'm not quite sure what is being defined here

```
#define DEFAULT_DIN_SR_PIN_01_08 2
#define DEFAULT_DIN_SR_PIN_09_16 3
#define DEFAULT_DIN_SR_PIN_17_24 1
#define DEFAULT_DIN_SR_PIN_25_32 4
#define DEFAULT_DIN_SR_PIN_33_40 8
#define DEFAULT_DIN_SR_PIN_41_48 10
#define DEFAULT_DIN_SR_PIN_49_56 11
#define DEFAULT_DIN_SR_PIN_57_64 12
```

Answer: MB64E supports up to 64 button functions, and they are assigned to 8 shift registers. With the

DEFAULT\_DIN\_SR\_\* settings you can change the mapping. This is important, if the appr. shift registers are already allocated by encoders (defined in mios\_tables.inc) - if this is the case, change the DEFAULT\_DIN\_SR\_\* values to unused shift register numbers.

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