An acronym for Simple Programmable Logic Device, or Simple PLD. SPLDs are the smallest and consequently the least-expensive form of programmable logic. They include PALs, PLAs, FPLAs and are often just called PLDs. The other, more powerful form of PLD is a CPLD. An SPLD is typically comprised of four to 22 macrocells and can typically replace a few 7400-series TTL devices. Each of the macrocells is typically fully connected to the others in the device. Most SPLDs use either fuses or non-volatile memory cells such as EPROM, EEPROM, or FLASH to define the functionality.

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