

See [mios_pic18f4685](#) for a description of the Microchip.



If anyone adapts MIOS and the Bootloader and certain Application Settings to this Chip, it would be kind to document it here.

Note: PIC18F4685 and PIC18F4682 are not 100% binary compatible to PIC18F452. RAM from 0x60-0x7f is not directly accessible. BANKED accesses have to be used instead.

See also:

- [mios pic18f452](#)
- [mios pic18f4620](#) and [using pic18f4620](#)
- [mios pic18f4650](#) (some informations about this can also be found on [using pic18f4620](#))
- [mios pic18f4685](#)

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